

REMARKS

In response to the Examiner's Office Action, the specification and claims have been amended to more clearly describe how the phase bandwidth reduction is accomplished in the present invention. Formal amended drawings are included.

As explained in the patent specification, when envelope elimination restoration (EER) is used in an amplifier circuit, the original input signal is decomposed into an amplitude component signal and phase component signal. The decomposition of the input signal results in expansion of the bandwidth of the output signal because rapid phase changes occur at low signal amplitudes. Applicant's invention reduces the bandwidth of the phase component signal by adding some of the amplitude modulation back into the phase component signal when the input signal has low amplitude. Eq. 2 on page 14 of the original specification relates the amplitude of the phase component signal to the normalized amplitude of the original input signal. Figure 1 of the application is a high level block diagram of the invention. Figures 3A and 3B provide further details for one exemplary embodiment. Figure 4 provides further details for a second exemplary embodiment.

To clarify the specification, some of the terminology has been changed and additional reference numbers have been added. In particular, in the original specification and claims, the term "phase bandwidth reduction module" was used inconsistently. In some instances, the term "phase bandwidth reduction module" referred to component 112, while in other instances, the term "phase bandwidth reduction module" referred to components 112 and 114 as a unit. A new box has been added around components 112 and 114, and has been labeled the phase bandwidth reduction module 111 in the specification. The component referred to as the "phase bandwidth reduction module" indicated by reference number 112 has been renamed "the look-up table" 112. Further, new boxes with the reference number 114 have been added to Figures 3A and 4 to indicate those components that correspond to the signal processing module in

Figure 1. These amendments do not add new matter to the application, but are made to eliminate some confusion caused by inconsistent use of terminology.

The claims have also been amended to state that the phase bandwidth reduction module adjust the amplitude of the phase component signal to reduce the bandwidth of the phase component signal.

Because the Examiner has not cited any prior art references disclosing the claimed invention, Applicant believes that the amendments place the application in condition for allowance and notice to such effect is respectfully requested.

Respectfully submitted,

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A handwritten signature in cursive script, reading "David E. Bennett", written over a horizontal line.

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